

FIG. 1

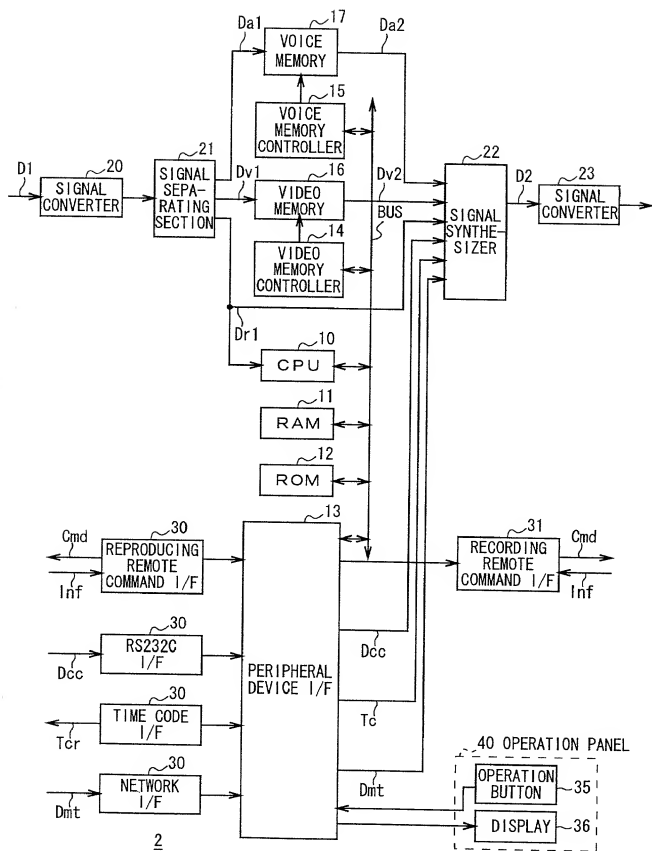


FIG. 2

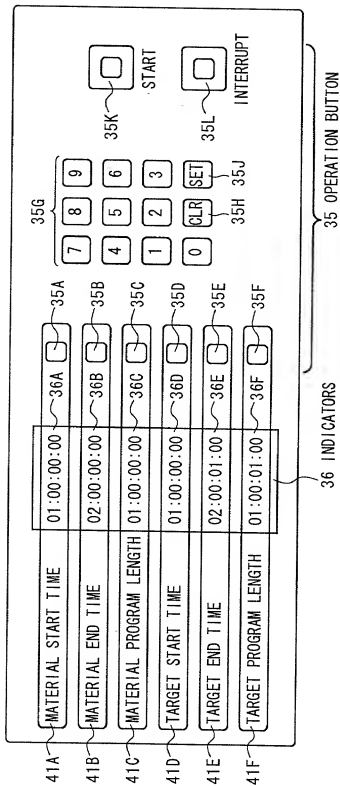


FIG. 3

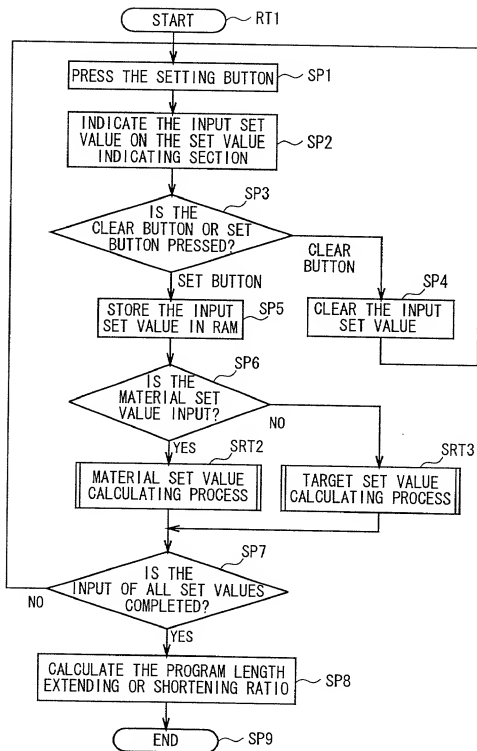


FIG. 4

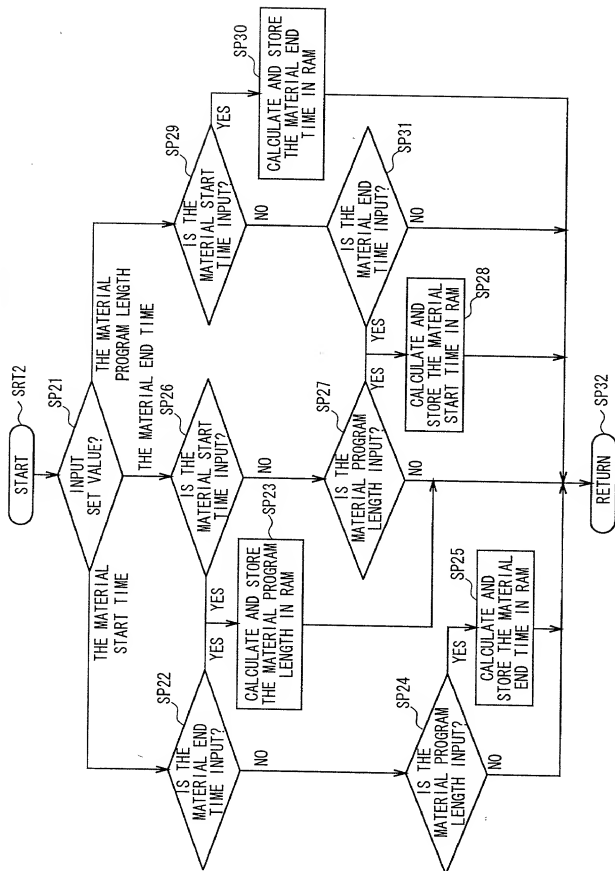


FIG. 5

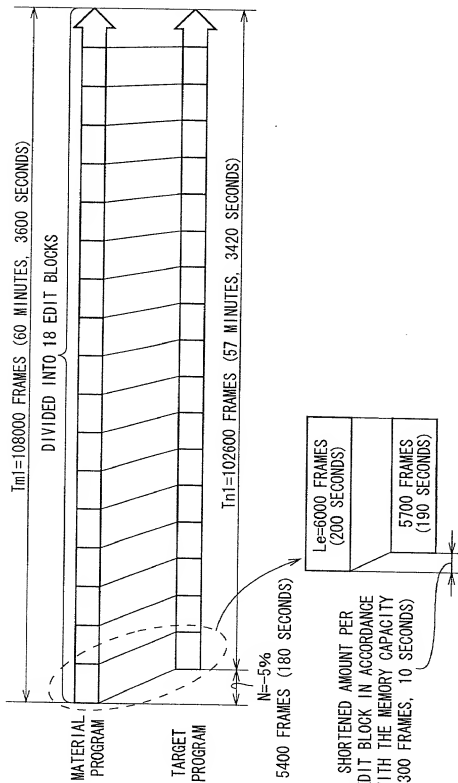


FIG. 6

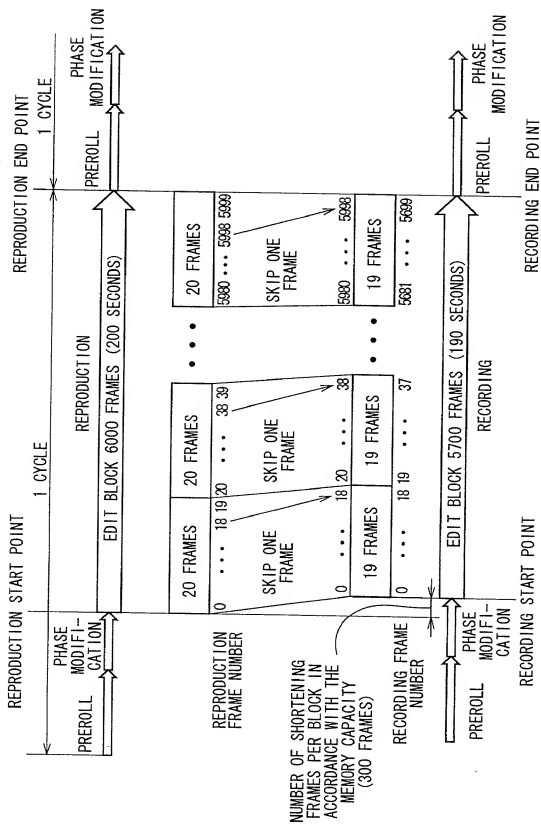


FIG. 7

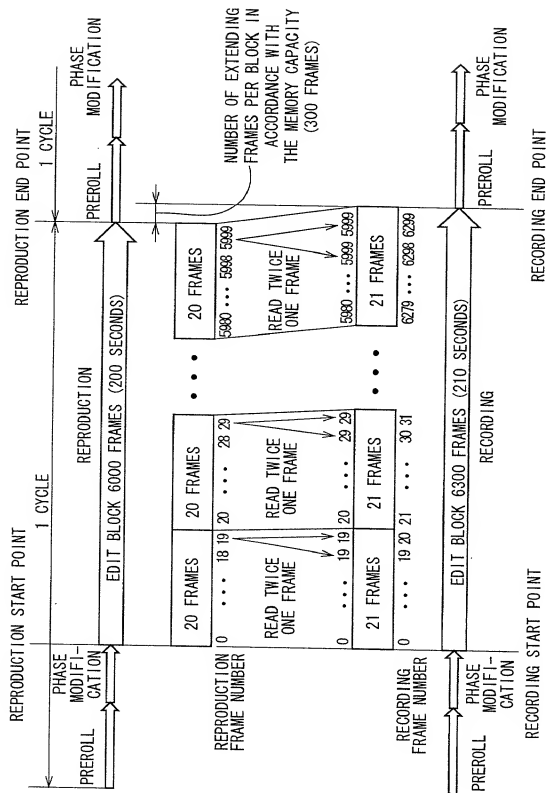


FIG. 8

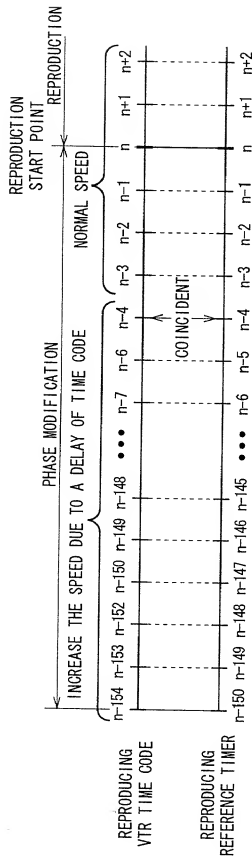


FIG. 9A

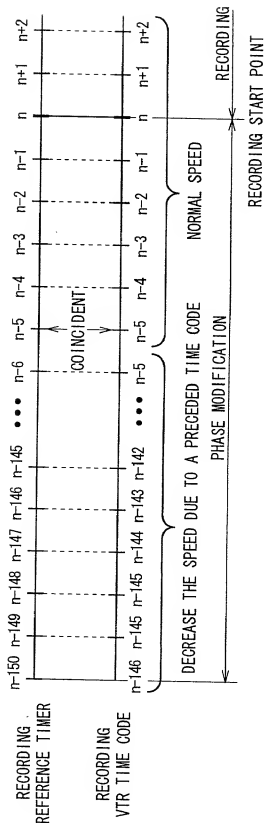


FIG. 9B

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graph TD
    RT4([START]) --> SP41[CALCULATE THE EDIT BLOCK LENGTH AND THE NUMBER OF CYCLES]
    SP41 --> SP42[PREROLL THE REPRODUCING AND RECORDING VTRS]
    SP42 --> SRT5[SRT5, SRT6]
    SRT5 --> SP43[START STORAGE IN THE VIDEO MEMORY]
    SP43 --> SP44[START READING FROM THE VIDEO MEMORY, START RECORDING OF THE RECORDING VTR, AND START OUTPUT OF THE TIME CODE]
    SP44 --> SP45[STOP THE REPRODUCING VTR AND THE RECORDING VTR]
    SP45 --> SP46{ARE ALL THE EDIT BLOCKS ENDED?}
    SP46 -- NO --> SRT5
    SP46 -- YES --> SP47([END])
```

FIG. 10

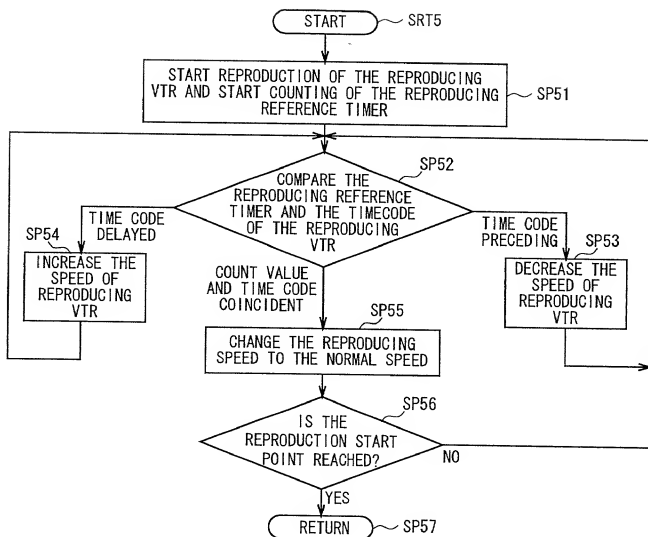


FIG. 11

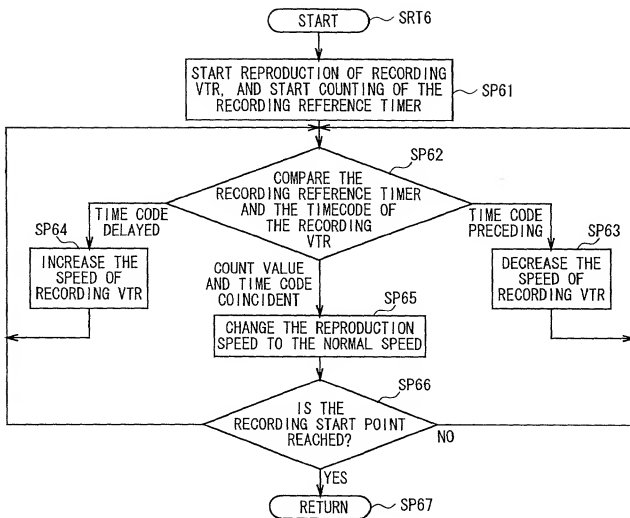


FIG. 12